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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10046792	FILING DATE 01/15/2002	CLASS 257	SUBCLASS 508	GAU 2841	EXAMINER <i>Julie Lee</i>
**APPLICANTS: Natsume Hidetaka; <i>Nguyen</i>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-013330 01/22/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO NECW 19.349	
TITLE : Semiconductor memory device and manufacturing method thereof <small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		JOSEPH NGUYEN		CLAIMS ALLOWED	
		Assistant Examiner		Total Claims 13	Print Claim for O.G. 1
ISSUE FEE				DRAWING	
Amount Due	Date Paid			Sheets Drwg. 38	Figs. Drwg. 65
		Primary Examiner		Print Fig. 1	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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